

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: James D. Ballew, et al.
Serial No.: 10/826,959
Filing Date: April 15, 2004
Art Unit: 2113
Confirmation No.: 1484
Examiner: Elmira Mehrmanesh
Title: **System and Method for Detecting and Managing HPC Node Failure**

Mail Stop: RCE

Commissioner of Patents
PO Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Information Disclosure Statement (IDS)

Applicants respectfully request, pursuant to 37 C.F.R. §§ 1.56, 1.97, and 1.98, that the references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. Pursuant to 37 C.F.R. §§ 1.97(g) and (h), Applicants make no representation that these documents qualify as prior art, that these documents are material to patentability of the present application, or that a search has been made.

The present application was filed after June 30, 2003. Therefore, under the July 11, 2003 waiver of 37 C.F.R. § 1.98(a)(2)(i) by the U.S. Patent and Trademark Office, copies of the U.S. Patents and U.S. Patent Application Publication listed in the attached PTO-1449 form are not enclosed. Copies of all other references are enclosed for the convenience of the Examiner.

This IDS is being filed concurrently with a Request for Continued Examination; therefore, Applicants believe no fee is due. However, the Commissioner is hereby authorized to charge any necessary fees and credit any overpayments to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,
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Date: November 30, 2009

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